

Notice of References Cited

Application/Control No.

10/519,700

Applicant(s)/Patent Under
Reexamination
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Examiner

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Art Unit

1765

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
1	U	S. Wolf, Silicon Processing for the VLSI Era, Vol.3, Lattice Press (1995), pages 207-208 and 222-223
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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